



Sagantec acquires NP-Komplete Technologies

Adding sophisticated 2D dynamic compaction technology to address 20nm and 14nm challenges

Santa Clara, California – May 3, 2012 – Sagantec today announced that it has acquired Dutch startup NP-Komplete Technologies BV (Eindhoven, The Netherlands) for its physical design compaction and migration solutions based on a sophisticated 2D dynamic compaction technology. NP-Komplete Technologies (NPKT) is a provider of innovative DFM solutions, and physical design optimization engines. Terms of the acquisition are confidential.

The Sagantec engineering team in the Netherlands is now fully integrated with the NPKT team with the combined team working together on enhancing the tools and addressing future technology challenges. Following the acquisition, NPKT is now a wholly-owned subsidiary of Sagantec and continues to develop and provide advanced layout compaction and DFM solutions.

The NPKT migration technology is an automated layout optimization technology, and was designed from the ground up to address the latest challenges of 20nm design rules and beyond. Since most of the new rules are enforced to overcome manufacturability-related challenges that impact yield, it takes a sophisticated 2D compaction technology to make necessary modifications to create a DRC clean layout. The NPKT technology has already been proven with DRC clean results at several tier-1 semiconductor companies, and used on multiple 28nm and 20nm technologies of leading-edge foundries.

The addition of this new technology and expertise enables Sagantec to better address the new challenges and overcome technology discontinuities that result from the introduction of new layers and new types of design rules associated with the 20nm process node while producing best possible quality results.

About Sagantec

Sagantec accelerates implementation of physical silicon IP in advanced process technologies. Sagantec's electronic design automation solutions enable a dramatic shortcut in the successful deployment of new silicon technology through the use of physical design reuse, automatic process migration, DRC correction and DFM optimization. Sagantec's process migration tools are used to retarget designs to either the next technology node or to a different process at the same or previous technology node. For library, memory, analog and mixed-signal IP designers, Sagantec enables the fastest migration path to any process technology down to 20nm. Privately held and funded, its corporate headquarters is at 2075 De La Cruz Blvd., Santa Clara, CA 95050. Telephone: (408) 727-6290 Fax: (408) 727-6288. On the Web at:<http://www.sagantec.com>.....

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